## FIG.1

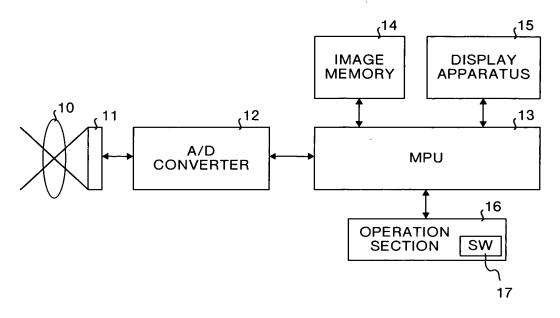
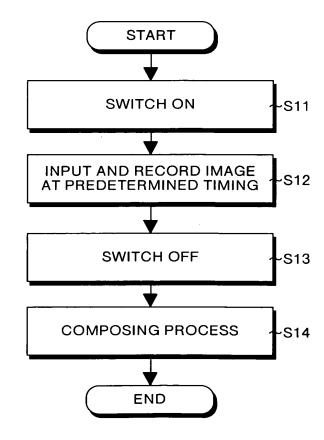
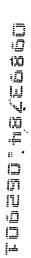


FIG.2





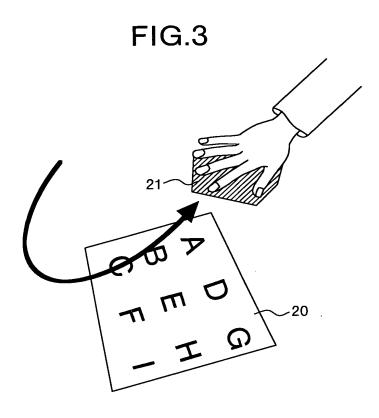


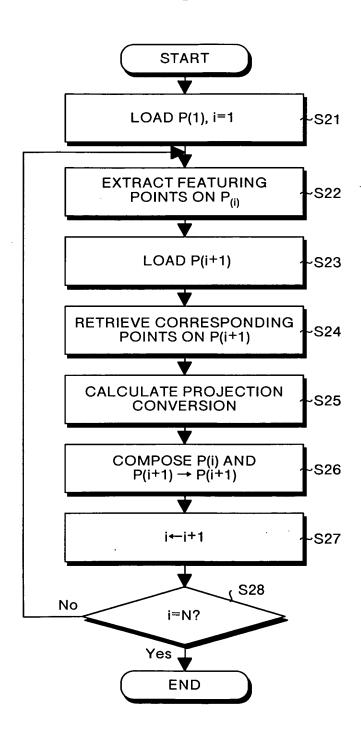
FIG.4A FIG.4B FIG.4C FIG.4D

A D B E E H D G

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FIG.5



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FIG.6A

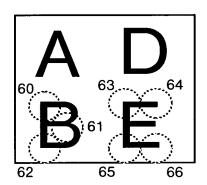


FIG.6B

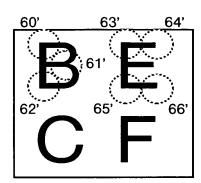


FIG.7A

A D B E C F FIG.7B

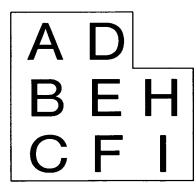


FIG.7C

A D G B E H C F I

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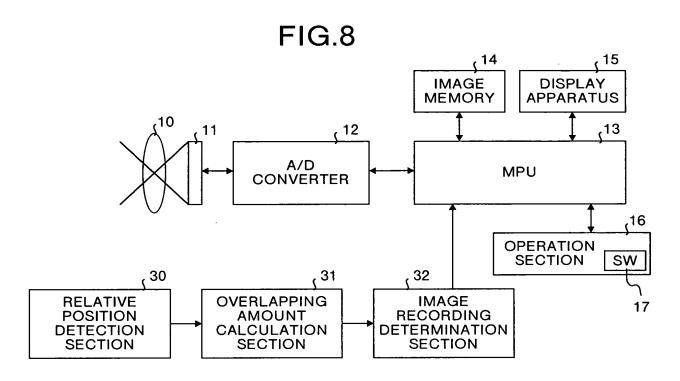
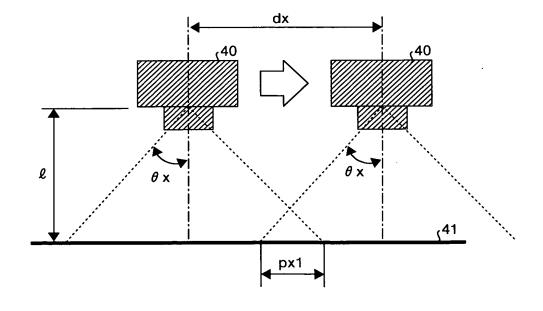
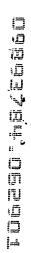
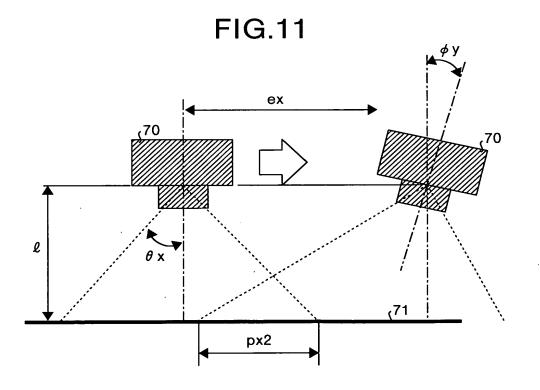


FIG.9

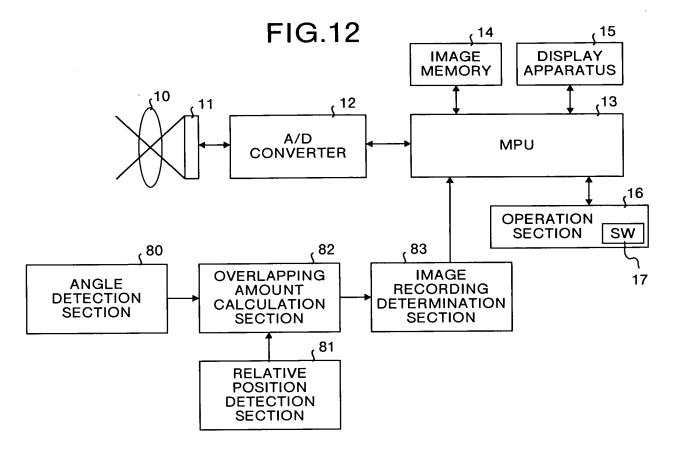


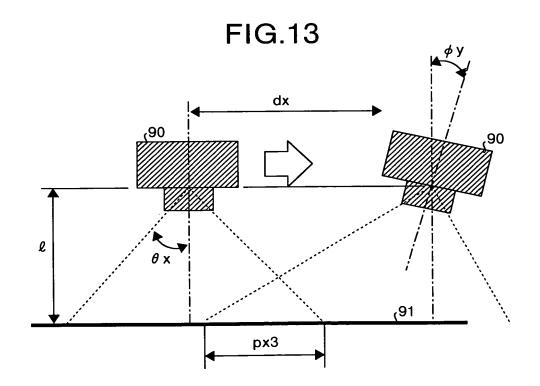


**FIG.10** , 15 **IMAGE DISPLAY MEMORY APPARATUS** ,13 , 11 , 12 A/D MPU CONVERTER ,16 **OPERATION SECTION** SW ,50 , 52 51 , **OVERLAPPING IMAGE** 17 **ANGLE AMOUNT** RECORDING **DETECTION** CALCULATION **DETERMINATION SECTION SECTION** SECTION



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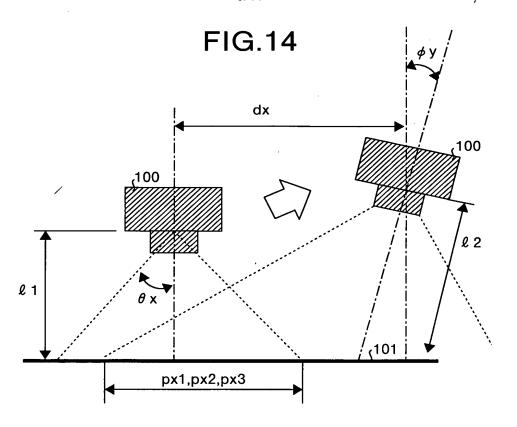


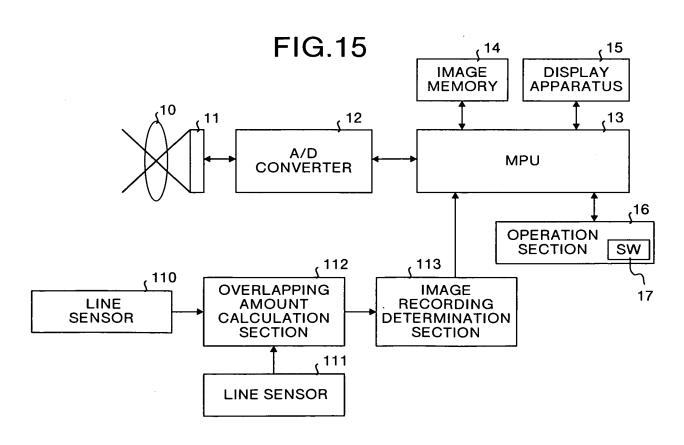


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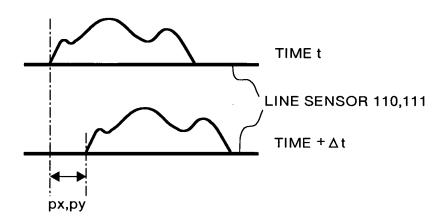
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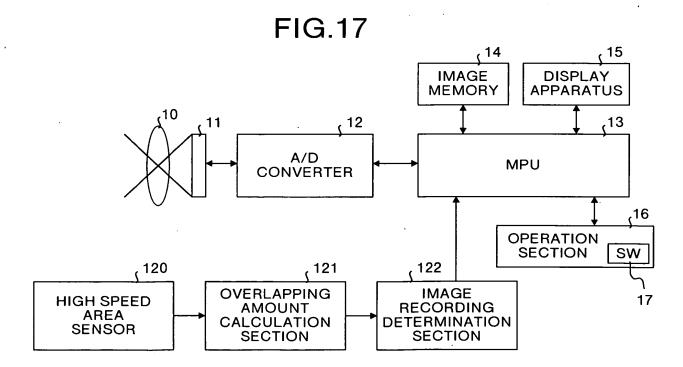






**FIG.16** 

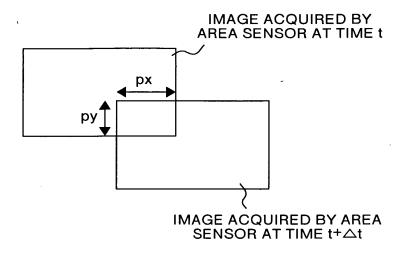




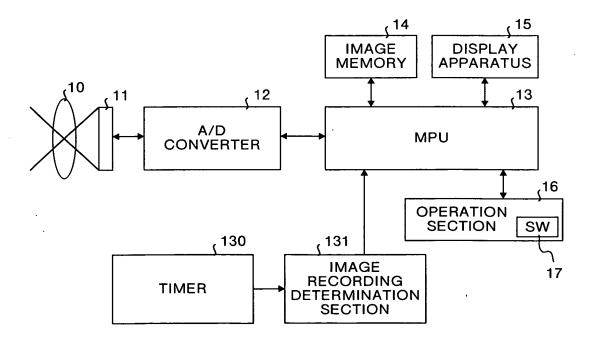
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**FIG.18** 



**FIG.19** 



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## **FIG.20**

